

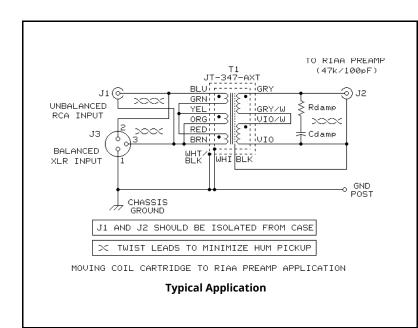
## Moving Coil Transformer

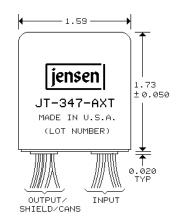
enser

1:12 CONFIGURATION FOR MC CARTRIDGES UP TO 40  $\Omega$ 

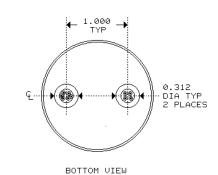
- Adds 21 dB of low noise, low distortion gain to RIAA preamps
- Wide bandwidth: -3 dB at 0.4 Hz and 100 kHz
- Deviation from Linear Phase 1° typical at 20 Hz
- Transformer Noise Figure only 1.2 dB @ 20  $\Omega,$  1.8 dB @ 12  $\Omega$
- High common-mode rejection: 125 dB at 60 Hz

This transformer is designed to match low impedance moving coil phono cartridges with 47 k $\Omega$  RIAA preamps. It is ideal for improving the signal-to-noise ratio of vacuum tube preamps. 60 dB nested double magnetic shielding is standard. See alternate data sheets for use with other impedance ranges.

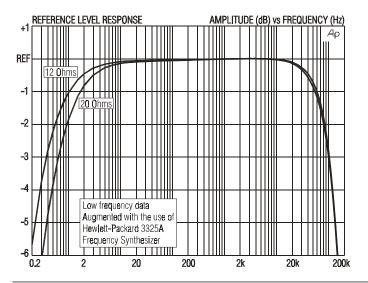


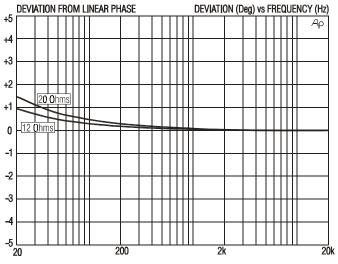


8 INCH #26 AWG (7×34) SILVER PLATED COPPER LEADS WITH TEFLON INSULATION.



RECOMMENDED MOUNTING IS WITH VR-4 CLAMP (SUPPLIED WITH TRANSFORMER)





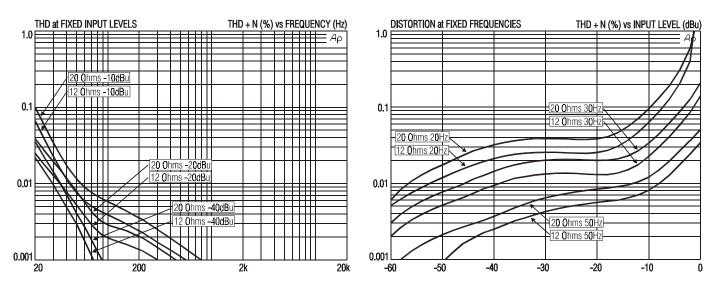


Jensen Transformers Inc., 9304 Deering Ave. Chatsworth, CA 91311 P: (818) 374-5857 F: (818) 374-5856 info@jensen-transformers.com

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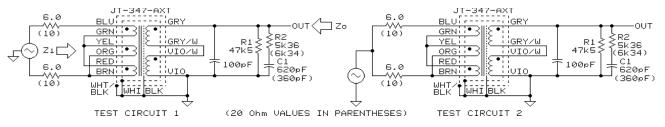
## JT-347-AXT



## JT-347-AXT SPECIFICATIONS (unless noted, apply for either 12 Ω or 20 Ω sources; all levels are input)

PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Zi	1 kHz, -40 dBu, test circuit 1	314 Ω	317 Ω	320 Ω
Voltage gain	1 kHz, -40 dBu, test circuit 1	20.7 dB	21.0 dB	21.3 dB
Magnitude response, ref 1 kHz	20 Hz, –40 dBu, test circuit 1 (12 $\Omega$ )	-0.16 dB	-0.06 dB	0.5 dB
	20 kHz, $-40$ dBu, test circuit 1 (12 $\Omega$ )	-0.20 dB	-0.11 dB	0.0 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, –40 dBu, test circuit 1 (12 $\Omega)$		+1.0°	±2°
Distortion (THD)	1 kHz, -40 dBu, test circuit 1 (12 Ω)		< 0.001%	
	20 Hz, -40 dBu, test circuit 1 (12 Ω)		0.025%	0.075%
Maximum 20 Hz input level	1% THD, test circuit 1 (12 $\Omega$ )	-7.0 dBu	-2.0 dBu	
Common-mode rejection ratio (CMRR)	60 Hz, test circuit 2 (12 $\Omega$ )		125 dB	
	3 kHz, test circuit 2 (12 $\Omega$ )	75 dB	95 dB	
Output impedance, Zo	1 kHz, Rs = $12 \Omega$ , test circuit 1		2.43k Ω	
	$1 \text{ kHz}, \text{Rs} = 20 \Omega$ , test circuit $1$		3.44k Ω	
DC resistances	3 primaries in series/parallel (per test circuits)		2.53 Ω	
	2 secondaries in series (GRY to VIO)		483 Ω	
Capacitances @ 1 kHz	3 primaries in series/parallel to shield and case		1,000 pF	
	2 secondaries in series to shield and case		245 pF	
Turns ratio	3 primaries in series/parallel (per test circuits)	1:11.996	1:12	1:12.004
Temperature range	operation or storage	0° C		70° C
Breakdown voltage (see IMPORTANT NOTE below)	primary or secondary to shield and case, 60 Hz, 1 minute test duration	250 V RMS		

IMPORTANT NOTE: This device is NOT intended for use in life support systems or any application where its failure could cause injury or death. The breakdown voltage specification is intended to insure integrity of internal insulation systems; continuous operation at these voltages is NOT recommended. Consult our applications engineering department if you have special requirements.



All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.



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