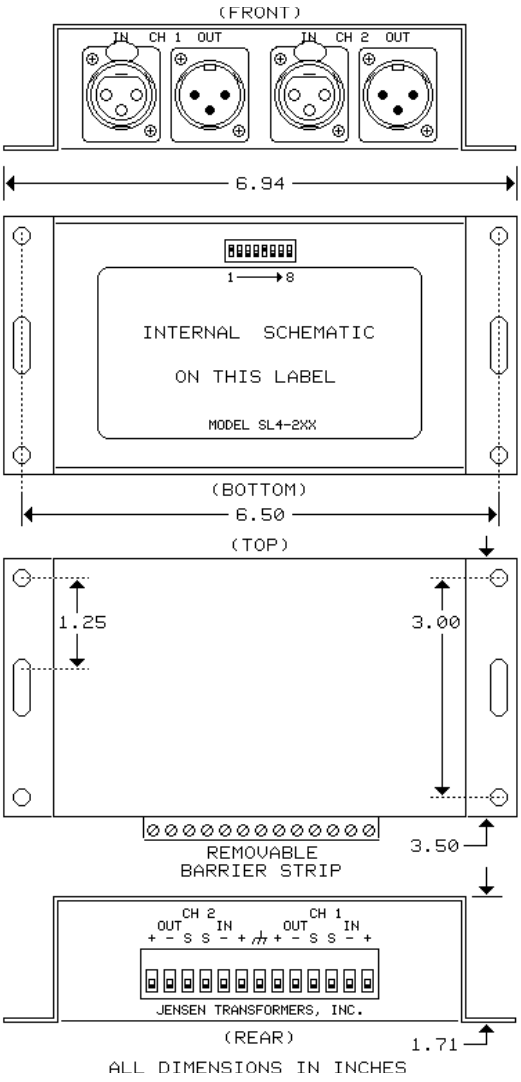
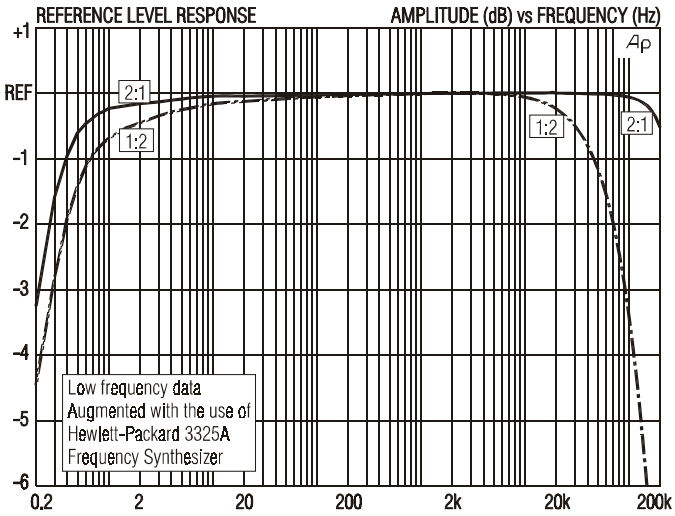
DUAL LINE OUTPUT ISOLATOR

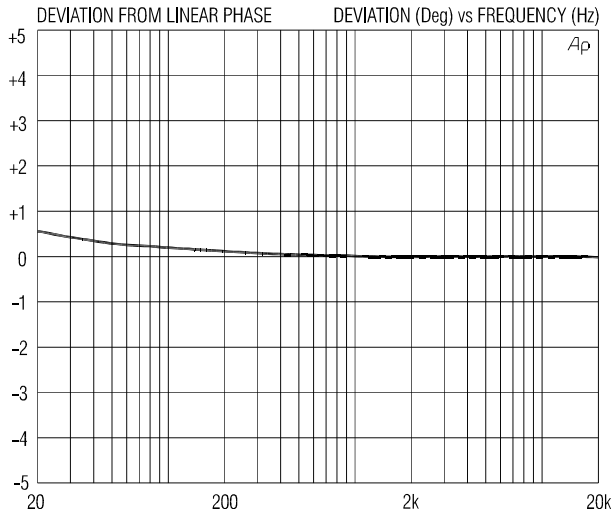
1:1, 1:2 and 2:1 CONFIGURABLE for SPECIAL APPLICATIONS

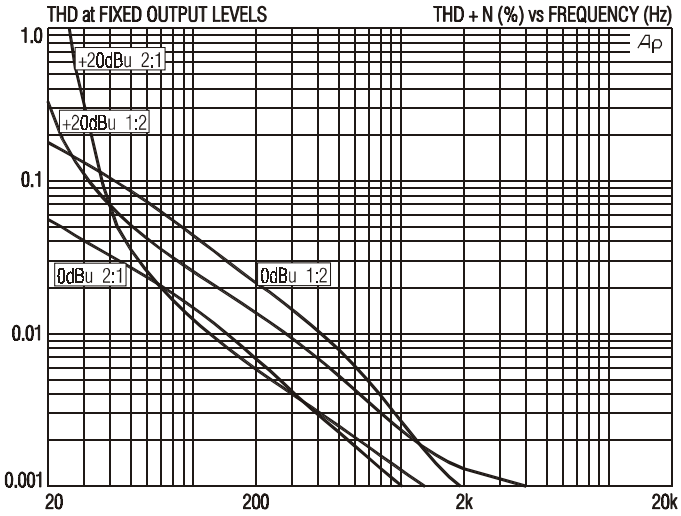
- **Distortion 0.003% typ at 1 kHz and +4 dBu output level(1:2)**
- **Wide bandwidth: -3 dB at 0.3 Hz and 100 kHz (1:2)**
- **Outputs up to +22 dBu at 20 Hz and +26 dBu at 30 Hz(1:2)**
- **Excellent time domain performance: DLP 0.6° typ 20 Hz to 20 kHz**
- **High Common Mode Rejection Ratio of 85dB**

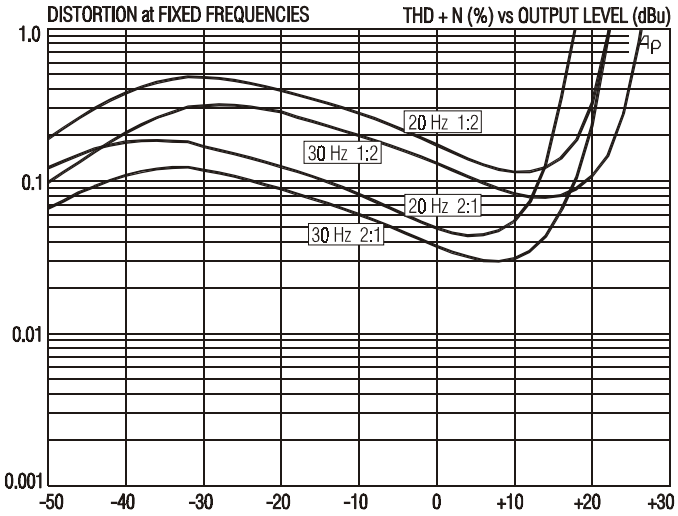
Internal jumpers allow configuring this isolator for 600:600 Ω, 150:600 Ω, or 600:150 Ω operation. It may be used with balanced or unbalanced sources and/or loads and has no cable length restrictions. It should be driven from low impedance sources and is **not** recommended for use with consumer electronics.





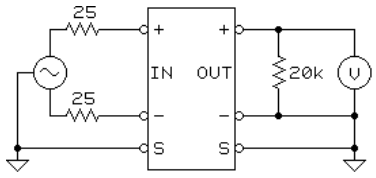




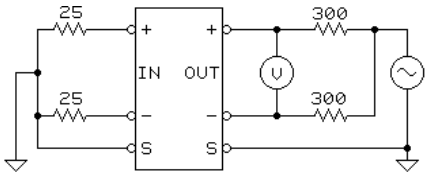


SL4-2XX SPECIFICATIONS (all levels are output, additional information on JT-123-SLPC Datasheet)

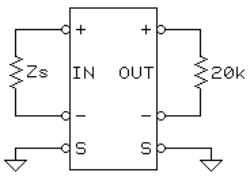
PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Recommended Source Impedance, Zs	1 kHz, 0 dBu, test circuit 3, 2 : 1 Mode	0 Ω	50 Ω	600 Ω
	1 kHz, 0 dBu, test circuit 3, 1 : 2 Mode	0 Ω	50 Ω	100 Ω
Output Impedance	1 kHz, 0 dBu, test circuit 4, 2 : 1 Mode		33 Ω	
	1 kHz, 0 dBu, test circuit 4, 1 : 2 Mode		282 Ω	
Voltage gain	1 kHz, 0 dBu, test circuit 1, 2 : 1 Mode	-6.10 dB	-6.00 dB	-5.90 dB
	1 kHz, 0 dBu, test circuit 1, 1 : 2 Mode	5.90 dB	6.00 dB	6.10 dB
20 Hz Magnitude response, referred to 1 kHz	0 dBu, test circuit 1, 2 : 1 Mode	-0.15 db	-0.05 dB	0.05 dB
	0 dBu, test circuit 1, 1 : 2 Mode	-0.20 dB	-0.10 dB	-0.00 dB
20kHz Magnitude response, referred to 1 kHz	0 dBu, test circuit 1, 2 : 1 Mode	-0.10 dB	0.02 dB	0.10 dB
	0 dBu, test circuit 1, 1 : 2 Mode	-0.10 dB	0.03 dB	0.10 dB
Distortion (THD)	1 kHz, +4 dBu, test circuit 1, 2 : 1 Mode		0.001%	
	1 kHz, +4 dBu, test circuit 1, 1 : 2 Mode		0.003%	
	20 Hz, +4 dBu, test circuit 1, 2 : 1 Mode		0.050%	0.200%
	20 Hz, +4 dBu, test circuit 1, 1 : 2 Mode		0.140%	
Maximum output level	20 Hz, 1% THD, test circuit 1, 2 : 1 Mode	+16 dBu	+18 dBu	
	20 Hz, 1% THD, test circuit 1, 1 : 2 Mode	+20 dBu	+22 dBu	
Common-mode rejection ratio (CMRR)	60 Hz, test circuit 2, All Modes		90 dB	
	3 kHz, test circuit 2, All Modes	70 dB	85 dB	
Temperature range	operation or storage	0° C		70° C
Input to Output Voltage Difference (see IMPORTANT NOTE below)	shield to shield or shield to chassis, 60 Hz			24 V RMS 34 V peak



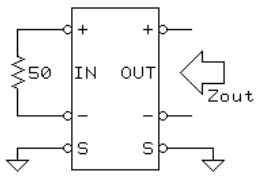
TEST CIRCUIT 1



TEST CIRCUIT 2



TEST CIRCUIT 3



TEST CIRCUIT 4

All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.

IMPORTANT NOTE: THIS PRODUCT IS NOT INTENDED FOR USE IN CIRCUMSTANCES WHERE THE DC OR PEAK AC VOLTAGE BETWEEN INPUT AND OUTPUT CONNECTIONS EXCEEDS 34 VOLTS OR WHERE ITS FAILURE COULD CAUSE INJURY OR DEATH.

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